

IN THE CLAIMS

Please amend the claims as follows, substituting any amended claim(s) for the corresponding pending claim(s):

1 Cancel Claims 1-17

1 18. (Previously presented) A method for transmitting non-synchronous events, comprising:  
2 building a fixed length user bit stream that reflects when synchronized events are to be  
3 transmitted;  
4 copying the fixed length user bit stream into a real time bit stream;  
5 determining what bit of the real time bit stream relates to a present time; and  
6 determining whether to initiate transmission of a non-synchronous communication event.

1 19. (Previously presented) The method of claim 18 further including copying the fixed length  
2 user bit stream into the real time bit stream on a periodic basis.

1 20. (Original) The method of claim 18 further including performing a mathematical operation  
2 as a part of determining what bit of the real time bit stream relates to the present time.

1 21. (Original) The method of claim 18 further including performing a mathematical operation  
2 to determine a group of bits of the real time bit stream that include what bit relates to the present time.

1 22. (Currently Amended) The method of claim 18 including ~~the step of~~ dividing the present  
2 time by a modulo number as a part of determining what bit in the real time bit stream relates to the  
3 present time.

1 23. (Previously presented) The method of claim 22 wherein the modulo number is equal to  
2 the number of bits in the fixed length user and the real time bit streams.

1 24. (Original) The method of claim 22 wherein the modulo number is equal to number "8".

1 25. (Previously presented) The method of claim 22 wherein a remainder determined during  
2 the dividing step identifies the specific bit of the real time bit stream that represents the present time.

1           26.     (New) A wireless transceiver device, comprising:  
2           memory for storing synchronous and non-synchronous events; and  
3           circuitry defining logic that includes:  
4                 building a fixed length user bit stream that reflects when synchronized events are  
5           to be transmitted;  
6                 copying the fixed length user bit stream into a real time bit stream;  
7                 determining what bit of the real time bit stream relates to a present time; and  
8                 determining whether to initiate transmission of a non-synchronous  
9           communication event.

1           27.     (New) The wireless transceiver device of claim 26 wherein the logic further includes  
2     copying the fixed length user bit stream into the real time bit stream on a periodic basis.

1           28.     (New) The wireless transceiver device of claim 26 wherein the logic further includes  
2     performing a mathematical operation as a part of determining what bit of the real time bit stream relates to  
3     the present time.

1           29.     (New) The wireless transceiver device of claim 26 wherein the logic further includes  
2     performing a mathematical operation to determine a group of bits of the real time bit stream that include  
3     what bit relates to the present time.

1           30.     (New) The wireless transceiver device of claim 26 wherein the logic further includes  
2     dividing the present time by a modulo number as a part of determining what bit in the real time bit stream  
3     relates to the present time.

1           31.     (New) The wireless transceiver device of claim 30 wherein the modulo number is equal  
2     to the number of bits in the fixed length user and the real time bit streams.

1           32.     (New) The wireless transceiver device of claim 30 wherein the modulo number is equal  
2     to number "8".

1           33.     (New) The wireless transceiver device of claim 30 wherein a remainder determined  
2     during the dividing step identifies the specific bit of the real time bit stream that represents the present  
3     time.